



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Edward R. Rhoads Et Al.

Serial No.: 10/764,617

Filed: January 26, 2004

For: Organizing Information Stored In
Non-Volatile Re-Programmable
Semiconductor Memories

§ Group Art Unit:

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Examiner:

Atty. Dkt. No.: ITL.0241D1US
(P7376D)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated January 10, 2001 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application. A check for \$180.00 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0241D1US).

Respectfully submitted,

February 23, 2004
Date


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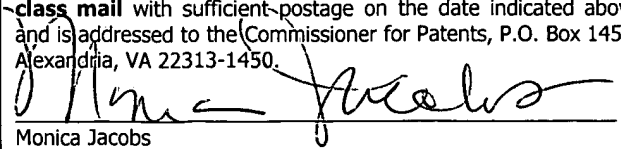
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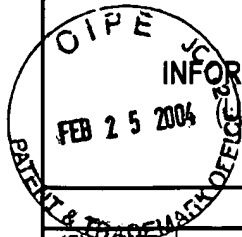
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I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Monica Jacobs

**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

ATTN: DOCKET NO.

ITL.0241D1US (P7376D)

SERIAL NO.

10/764,617

APPLICANT(S):

Edward R. Rhoads et al.

FILING DATE:

January 26, 2004

GROUP ART UNIT:

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.	5,745,418	04/28/1998	Ma et al.			
	B.						
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	I.							
	J.							
	K.							
	L.							
	M.							
	N.							
	O.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	P.	E. Auzas, <i>Die Kunst Des Richtigen Dimensionierens. Bausteine Fur Die Entwicklung Eines Embedded-PC</i> , Elektronik, De, Franzis Verlag GMBH, Munchen, Vol. 45, No. 3, Feb. 6, 1996, pgs. 66-75.
	Q.	
	R.	
	S.	
	T.	
	U.	
	V.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.